

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Application Serial No. .... 10/050,347  
Filing Date ..... January 15, 2002  
Inventor..... Gurtej S. Sandhu et al.  
Assignee..... Micron Technology, Inc.  
Group Art Unit..... 2813  
Examiner ..... Schillinger, Laura M.  
Attorney's Docket No. .... MI22-1897  
Customer No. .... 021567  
Title: Methods of Forming Transistors

**RESPONSE TO FEBRUARY 6, 2007 NOTICE OF ALLOWANCE**  
**TO ACCOMPANY RCE FILING**

To: Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

From: Jennifer J. Taylor, Ph.D. (Tel. 509-624-4276; Fax 509-838-3424)  
Wells St. John P.S.  
601 W. First Avenue, Suite 1300  
Spokane, WA 99201-3828

**AMENDMENTS**

**Introductory Comments**

In reply to the Notice of Allowance of February 6, 2007, applicant amends and remarks as follows.